

SPECIFICATION

The following electrical characteristics (Table 8) are valid only if the instrument has been calibrated at an ambient temperature between $+20^{\circ}\text{C}$ and $+30^{\circ}\text{C}$, the instrument is operating at an ambient temperature between -15°C and $+55^{\circ}\text{C}$ (unless otherwise noted), and the instrument has had a warmup period of about 20 minutes.

Environmental characteristics are given in Table 9. The 468 meets the requirements of MIL-I-28800B, Class 3, Style D equipment. Physical characteristics are listed in Table 10, and option electrical characteristics are presented in Table 11.

Table 8

ELECTRICAL CHARACTERISTICS

Characteristics	Performance Requirements	Supplemental Information
VERTICAL SYSTEM		
Deflection Factor (Nonstorage Mode)		
Range		5 mV per division to 5 V per division in a 1-2-5 sequence of 10 steps.
DC Accuracy	Graticule indication is within 3% of true input voltage up to ± 12 divisions, referenced to instrument ground, for all calibrated VOLTS/DIV switch settings.	Gain set with VOLTS/DIV switch set to 5 mV per division.

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
VERTICAL SYSTEM (cont)		
Deflection Factor (cont) Uncalibrated (VAR) Range (Nonstorage Mode)	Continuously variable between settings of VOLTS/DIV switch. Extends deflection factor to at least 12.5 V per division.	
Low-Frequency Linearity		0.1 division or less compression or expansion of a 2-division signal at center screen with waveform positioned to upper and lower extremes of graticule area.
Frequency Response		5-division reference signal from a 25 Ω source; centered vertically, with VAR VOLTS/DIV control in calibrated detent.
Bandwidth (Channel 1 and Channel 2 Nonstorage Mode)		
-15°C to +40°C	Dc to at least 100 MHz.	
+40°C to +55°C	Dc to at least 85 MHz. ^a	

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
VERTICAL SYSTEM (cont)		
Frequency Response (cont) AC Coupled Lower 3 dB Point		
1X Probe	10 Hz or less.	
10X Probe	1 Hz or less.	
Step Response (Non-Storage Mode)		5-division reference signal, dc coupled at all deflection factors, from a 25 Ω source; vertically centered with VAR VOLTS/DIV control in calibrated detent.
Risetime 0°C to +40°C	3.5 ns or less (calculated). ^a	Rise Time = $\frac{0.35}{BW \text{ (in MHz)}}$
Positive-Going Step (Excluding ADD Mode)		
Aberrations 0°C to +40°C		+4%, -4%, 4% p-p or less, +6%, -6%, 6% p-p or less (5 V setting only).

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
VERTICAL SYSTEM (cont)		
Step Response (cont) Position Effect 0°C to +40°C		Total aberrations less than +6%, -6%, 6% p-p; checked at 5 mV per division.
Negative-Going Step		Add 2% to all positive-going specifica- tions; checked at 5 mV per division.
ADD Mode Operation		Add 5% to all aberration specifica- tions; checked at 5 mV per division.
Common Mode Rejection Ratio (ADD Mode With Channel 2 Inverted)		At least 10:1 at 20 MHz for common mode signals of 6 divisions or less with GAIN adjusted for best CMRR at 50 kHz. (10:1 at 10 MHz for storage mode.)
Trace Shift as VAR VOLTS/DIV Control Is Rotated		1 division or less. Digital Storage scale-factor LED will indicate voltage measurements are in divisions in a storage mode with the VAR control out of calibrated detent.

Table B (cont)

Characteristics	Performance Requirements	Supplemental Information
VERTICAL SYSTEM (cont)		
INVERT Trace Shift		Less than 2 divisions when switching from non-inverted to inverted.
Input Gate Current +20°C to +30°C		0.5 nA or less (0.1 division or less trace shift when switching input coupling between dc and GND with VOLTS/DIV switch set to 5 mV per division).
-15°C to -55°C		4 nA or less (0.8 division or less trace shift when switching input coupling between dc and GND with VOLTS/DIV switch set to 5 mV per division).
Channel Isolation		At least 100:1 at 25 MHz (10 MHz in storage).
Vertical POSITION Range		At least ± 12 and 12 divisions from graticule center.
Chopped Mode Repetition Rate (Nonstorage Mode)	Approximately 500 kHz.	Within 20%

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
VERTICAL SYSTEM (cont)		
Input R and C		
Resistance	1 M Ω , within 2%. ^a	
Capacitance	Approximately 20 pF. ^a	Within 3%.
R and C Product (-20°C to $+30^{\circ}\text{C}$)		Aberrations 2% or less using a P6105 probe.
Maximum Input Voltage DC Coupled	250 V (dc — peak ac). ^a 500 V (p-p ac at 1 kHz or less). ^a	
AC Coupled	250 V (dc — peak ac). ^a 500 V (p-p ac at 1 kHz or less). ^a	
Cascaded Operation		CH 1 VERT OUT SIGNAL OUT coupled into CH 2 input; ac coupled, using 50 Ω , 42-inch RG-58 C/U cable, terminated in 50 Ω at the CH 2 input connector.
Bandwidth (Nonstorage)	Dc to at least 50 MHz.	
Cascaded Sensitivity	At least 1 mV per division; terminated in 50 Ω at the CH 2 input connector.	

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
DIGITAL STORAGE VERTICAL ACQUISITION		
Resolution		8 bits, 25 levels per division, 10.24 divisions dynamic range.
DC Accuracy	Scaled binary value of stored digital word is within 3% of true input voltage up to ± 12 divisions, referenced to instrument ground, for all calibrated VOLTS/DIV switch settings.	Gain set with VOLTS/DIV set to 5 mV per division.
Range		0.5 mV to 5 V per division in a 1-2-5 sequence of 13 steps.
Digital Sample Rate		10 Hz to 25 MHz as determined by the TIME/DIV switch setting.
Digital Chop Rate		5 Hz to 12.5 MHz (1/2 of the non-chopped sample rate at all TIME/DIV switch settings).
Analog Step Response	3% or less acquired overshoot on a 5-division pulse with Display Response set to PULSE.	Checked on a saved waveform display using horizontal expander (X10 MAG off).

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information				
DIGITAL STORAGE VERTICAL ACQUISITION (cont)						
Analog Bandwidth	Dc to 10 MHz, with +1 dB, measured in ENVELOPE Storage Mode with the TIME/DIV switch set to 1 ms.	At exactly 10 MHz input signal frequency, it is possible for aliasing to occur and produce an envelope with variable amplitude. If aliasing occurs, shift the test frequency slightly to obtain an envelope with flat amplitude.				
Useful Storage Bandwidth						
NORM Storage Mode						
SINE Display Response	<table><tr><th>Single Trace or Alt</th><th>CHOP</th></tr><tr><td>Dc to 10 MHz, with -1, -3 dB, measured p-p over any single cycle, with TIME/DIV switch set to 0.2 μs (X10 MAG off)</td><td>Dc to 5 MHz, with -0.5, -1.5 dB, measured p-p over any single cycle, with TIME/DIV switch set to 0.2 μs (X10 MAG off)</td></tr></table>	Single Trace or Alt	CHOP	Dc to 10 MHz, with -1, -3 dB, measured p-p over any single cycle, with TIME/DIV switch set to 0.2 μ s (X10 MAG off)	Dc to 5 MHz, with -0.5, -1.5 dB, measured p-p over any single cycle, with TIME/DIV switch set to 0.2 μ s (X10 MAG off)	For SINE Display Response, useful storage bandwidth is limited to that frequency where there are 2.5 samples per input cycle period at the maximum sampling rate (max sampling rate is 25 MHz in Single Trace or ALT and 12.5 MHz in CHOP).
Single Trace or Alt	CHOP					
Dc to 10 MHz, with -1, -3 dB, measured p-p over any single cycle, with TIME/DIV switch set to 0.2 μ s (X10 MAG off)	Dc to 5 MHz, with -0.5, -1.5 dB, measured p-p over any single cycle, with TIME/DIV switch set to 0.2 μ s (X10 MAG off)					
		Accuracy at useful storage bandwidth limit is measured with respect to a 6 division, 50 kHz reference sine wave.				

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
DIGITAL STORAGE VERTICAL ACQUISITION (cont)		
Useful Storage Bandwidth (cont)		
PULSE Display Response	Dc to 3.5 MHz, within ± 0.5, 1.5 dB, measured p-p over any single cycle, with TIME/ DIV switch set to 0.2 μs (X10 MAG off). Dc to 1.75 MHz, within ± 0.5, -1.5 dB, measured p-p over any single cycle, with TIME/ DIV switch set to 0.2 μs (X10 MAG off).	For PULSE Display Response, useful storage bandwidth is limited to that frequency where there are 7 samples per input cycle period at the maximum sampling rate (max sampling rate is 25 MHz in Single Trace or ALT and 12.5 MHz in CHOP). Accuracy at useful storage bandwidth limit is measured with respect to a 6-division, 50 kHz reference sine wave. Useful storage rise time is defined as 1.6 times the minimum sampling interval (40 ns in Single Trace or ALT and 80 ns in CHOP).
Useful Storage Rise Time		
NORM Storage Mode		
PULSE Display Response	64 ns.	128 ns.

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
TRIGGERING		
Sensitivity		In EXT/10, multiply requirements by 10.
AC Coupled Signal	0.3 division internal or 50 mV external from 30 Hz to 10 MHz; increasing to 1.5 divisions internal or 150 mV external up to 100 MHz.	
LF REF Coupled Signal	0.5 division internal or 100 mV external from 50 kHz to 10 MHz; increasing to 1.5 divisions internal or 300 mV external up to 100 MHz.	Attenuates signals below approximately 50 kHz.
HF REF Coupled Signal	0.5 division internal or 100 mV external from 30 Hz to 50 kHz.	Attenuates signals above approximately 50 kHz.
DC Coupled signal	0.3 division internal or 50 mV external from dc to 10 MHz; increasing to 1.5 divisions internal or 150 mV external up to 100 MHz.	

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
TRIGGERING (cont)		
Trigger Jitter		
Nonstorage Mode	0.5 ns or less at 100 MHz at 2 ns per division (X10 MAG on).	
Storage Mode	± 1 sample period for data transmitted on the GPIB. See Jitter Correction Performance Requirement. ^a	Inherent ± 1 sample jitter between sample clock and asynchronous trigger is partially compensated for by the jitter correction circuitry.
External Trigger Inputs		
Maximum Input Voltage	250 V (dc + peak ac). ^a 250 V (p-p ac at 1 kHz or less). ^a	
Input Resistance	1 M Ω within 10%. ^a	
Input Capacitance		Approximately 20 pF, within 30%.
LEVEL Control Range		
EXT	At least — and —2 V, 4 V p-p.	
EXT/10	At least + and —20 V, 40 V p-p.	

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
TRIGGERING (cont)		
A External Trigger View (Nonstorage Mode Only)		
Deflection Factor		Dc trigger coupling only; checked with a 1 kHz signal.
EXT	100 mV per division $\pm 5\%$.	
EXT/10	1 V per division $\pm 5\%$.	
Rise Time	5 ns or less. ^a	BW Limit at full (button out).
Delay Difference	± 0.20 division (± 300 ps at 2 ns per division).	5-division signal with 5 ns rise time or less from a 25 Ω source; centered vertically with equal 50 Ω cable length from signal source to vertical channel and external trigger input connectors; terminated in 50 Ω at each input.
Centering of Triggering Point		Within 1 division of center screen.
Flatness and Aberrations		$\pm 10\%$, 10%, 10% p-p.

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
HORIZONTAL DEFLECTION SYSTEM		
Sweep Rate (Nonstorage Mode) Calibrated Range A Sweep		0.5 s per division to 0.02 μ s per division in 23 steps in a 1-2-5 sequence. X10 MAG extends maximum sweep rate to 2 ns per division.
B Sweep		50 ms per division to 0.02 μ s per division in 20 steps in a 1-2-5 sequence. X10 MAG extends maximum sweep rate to 2 ns per division.
Accuracy	Within the given percentages of the indicated value.	Accuracy specification applies over the full 10 div of the unmagnified sweep.
+20°C to +30°C -15°C to +55°C	Unmagnified Within 2% Within 3% ^a	In X10 MAG, at TIME/DIV switch setting of .02 μ s, 1 μ s, and .2 μ s, exclude the first and last 50 ns of the sweep; and at a TIME/DIV switch setting of .5 μ s, exclude the first 100 ns of the sweep.
	Magnified Within 3% Within 4% ^a	

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
HORIZONTAL DEFLECTION SYSTEM (cont)		
Sweep Rate (cont) Two-Division Linearity Check		$\pm 5\%$ over any two-division portion (or less) of the full 10 divisions. When in X10 MAG exclude first and last magnified divisions when checking 2 ns, 5 ns, and 10 ns per division rates.
Alternate Sweep Trace Separation (Nonstorage Mode Only)		≥ 4 divisions.
Variable Range (A Only) (Nonstorage Mode)	Continuously variable between calibrated settings of the A TIME/DIV switch. Extends slowest A sweep rate to at least 1.25 s per division.	At least 2.5:1.
A Sweep Length (Nonstorage Mode)		10.5 to 11.5 divisions.
A Trigger HOLDOFF (Variable)		Increases A sweep holdoff time by at least a factor of 10 (Nonstorage Mode). Storage holdoff time is a function of microprocessor operation.
Magnifier Registration		Within 0.2 division from graticule center (X10 MAG on to X10 MAG off).

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
HORIZONTAL DEFLECTION SYSTEM (cont)		
POSITION Range (Horizontal)		Start of sweep must position to right of graticule center. End of sweep must position to left of graticule center.
Differential Time Measurement Accuracy (Nonstorage Mode) 15°C to +35°C 15°C to +55°C	Measurements of 1 or more major dial divisions	With the A TIME/DIV switch at 0.5 μ s per division, or 0.2 μ s per division, the differential time measurement accuracy limit is valid only for DELAY TIME POSITION dial settings between 1.50 and 8.50.
	Measurements of less than 1 major dial division	
	With 1% of indicated value. Within 2.5% of indicated value. ^a	

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
HORIZONTAL DEFLECTION SYSTEM (cont)		
Delay Time Jitter (Nonstorage Mode)	One part or less in 50,000 (0.002% of 10 times the A TIME/DIV switch setting) when operating on an ac-power-source frequency above 50 Hz. One part of less in 20,000 (0.005% of A TIME/DIV switch setting) when operating on a 50 Hz or lower ac-power source frequency. ^a	
Calibrated Delay Time (VAR Control in Calibrated Detent)	Continuous from 0.2 μ s to at least 5 s after the delaying (A) sweep.	
X-Y Operation (Nonstorage Mode Only)		
X-Axis Deflection Factor	Same as vertical system, with X10 MAG off.	
Variable Range	Same as vertical system.	
X-Axis Bandwidth	Dc to at least 4 MHz.	10 division reference signal.

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
HORIZONTAL DEFLECTION SYSTEM (cont)		
X-Y Operation (cont)		
Input Resistance	Same as vertical system. ^a	
Input Capacitance	Same as vertical system. ^a	
Maximum Usable Input Voltage	Same as vertical system. ^a	
Phase Difference Between X and Y Amplifiers		Within 3° from dc to 50 kHz.
Deflection Accuracy	Graticule indication is within 4% of true input voltage.	

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
DIGITAL STORAGE HORIZONTAL ACQUISITION		
Horizontal Resolution Single Waveform Acquisition		9 bit. 512 data points (50 data points per division across the graticule area).
Chopped Acquisition (NORM Storage Mode Only)		8 bit. 256 data points per division (25 data points per division across the graticule area).
Range		5 s per division to 20 ns per division in a 1-2-5 sequence. At TIME/DIV switch settings of 5 s to 2 μ s, waveform sampling rate is determined by the switch setting. From 1 μ s to 0.02 μ s per division, sampling rate is at the 2 μ s per division rate. Interpolation and analog gain are used to expand the signal to the correct horizontal scale.

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
DIGITAL STORAGE HORIZONTAL ACQUISITION (cont)		
Accuracy (Sample Period)		Sample clock is within 0.01% of selected sample period, ± 50 ps ADC aperture uncertainty.
		Crystal oscillator: 0°C to +70°C $V_{cc} - +5\text{ V} \pm 0.5\text{ V}$.
Dynamic Range	10.24 divisions.	
STORAGE DISPLAY		
Vertical Resolution		1 part in 1024 (10 bit). Calibrated for 100 points per division.
Differential Accuracy	Graticule indication of voltage cursor difference is within 2% of LED readout value, measured over center six divisions.	

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
STORAGE DISPLAY (cont)		
Vertical (cont) POSITION Range		Any portion of a stored waveform vertically magnified X10 can be positioned to the top and to the bottom of the graticule area.
Position Registration NON STORE to NORM		Within ± 0.5 division at graticule center at VOLTS/DIV settings from 5 mV to 5 V per division.
NORM, ENVELOPE, or AVG to SAVE		Within ± 0.2 divisions at VOLTS/DIV settings from 5 mV to 5 V per division.
SAVE Mode Gain Range (Vertical)		Up to X10 as determined by the setting of the VOLTS/DIV switch.
ENVELOPE Fill		90% or more of a six division ENVELOPE display.

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
STORAGE DISPLAY (cont)		
Vertical (cont) Rise Time		≤ 0.3 horizontal graticule division for a five-division step, with horizontal X10 MAG on. Checked with no samples on the rising edge of the waveform. +6%, --6%, 8% p-p or less on a five-division step (fast rise) input.
Aberrations		
Horizontal Resolution		1 part in 1024 (10 bit). Calibrated for 100 points per division.
Differential Accuracy	Graticule indication of time cursor difference is within 2% of LED readout value, measured over center eight divisions.	
SAVE Mode Gain Range (Horizontal)		Up to X100 as determined by the setting of the TIME/DIV switch.

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
STORAGE DISPLAY (cont)		
Horizontal (cont) Position Registration		Sweep start between NON STORE and Storage within ± 0.2 division at TIME/DIV switch setting of 1 ms.
Display Response (Selectable) SINE		Microprocessor performs an interpolation between data points that is optimized to produce the best response for input signals that have no frequency components above $F_s/2$, when F_s is the sampling rate. For a 6-division, sinusoidal input digitized at 2.5 samples per input cycle and expanded 10X with the TIME/DIV switch, SINE Display Response envelope distortion produces a maximum amplitude error at any peak which is less than 5% of the ideally reconstructed reference p-p amplitude, assuming no distortion in the acquired input signal.

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
STORAGE DISPLAY (cont)		
Display Response (cont) PULSE		Microprocessor performs linear interpolation between data points to optimize the display response for fast-rise and fast-fall waveforms (rise and fall times faster than 3 times the sampling interval). For a 6-division sinusoidal input at seven samples per input cycle period, PULSE Display Response envelope distortion produces a maximum amplitude error at any peak which is less than 5% of the ideally reconstructed reference p-p amplitude.

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
STORAGE DISPLAY (cont)		
Jitter Correction		Reduces effect of sample clock-to-trigger jitter.
Gain		0.4 division, $\pm 10\%$; X10 MAG on.
Resolution		± 0.1 sample period for TIME/DIV switch settings of 20 μs to 5 s per division. ± 3 ns for switch settings of 0.02 μs to 10 μs per division. NOTE <i>Due to inherent uncertainty involved in the jitter correction, the resolution will occasionally, at random intervals, exceed the limits given above.</i>

Table B (cont)

Characteristics	Performance Requirements	Supplemental Information
STORAGE DISPLAY (cont)		
Display Type		Four-digit, seven-segment LED indicators.
VOLTS Readout		Displays calculated voltage difference between horizontal cursors in VOLTS measurement mode. Scale factor is determined by VOLTS/DIV switch setting.
Resolution		1 part in 1024 (10 bit).
TIME Readout		Displays calculated time difference between cursor dots in TIME measurement mode. Scale factor determined by setting of the appropriate TIME/DIV switch (A or B).

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
STORAGE DISPLAY (cont)		
TIME Readout (cont) Resolution		1 part in 1024 (10 bit).
		NOTE Scale-factor LED indicates measurement is in DIV in the VOLTS measurement mode when vertical UNCAL LED is illuminated, or are used in a dual-channel mode.
	CRT DISPLAY	
Display Area		8 X 10 cm.
Geometry		0.1 division or less of tilt or bowing.
Trace Rotation Range		Adequate to align trace with horizontal graticule lines. At least $\pm 3^\circ$.

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
CRT DISPLAY (cont)		
Standard Phosphor		P31 (green).
Nominal Accelerating Potential		18.5 kV.
Electrode Voltages to Ground		
Heater Voltage Between CRT Pins 1 and 14		6.3 Vrms, +3 V; elevated to -2450 V.
Cathode (Pin 2)		-2450 V, +2%.
Grid No. 1 (Pin 3)		≈ -2455 V to -2555 V.
Focus (Pin 4)		≈ -1780 V to -2000 V.
Astigmatism (Pin 5)		0 V to $\approx +95$ V.
Isolation Shield (Pin 7)		+35 V, +5 V.
First Anode (Pin 8)		$\approx +55$ V.
Geometry (Pin 10)		0 to $\approx +95$ V.
Mesh (Pin 12)		≈ -150 V.

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
CALIBRATOR		
Output voltage 0°C to +40°C	0.3 V, within 1.0%	Within 0.5% at 25°C, +5°C.
—15°C to +55°C		0.3 V, within 1.5%.
Repetition Rate	Approximately 1 kHz.	Within 25%.
Output Resistance		Approximately 10.3 Ω .
Output Current +20°C to +30°C	30 mA, within 2%. ^a	
—15°C to —55°C		30 mA, within 2.5%.

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
Z-AXIS INPUT		
Sensitivity	5 V p-p signal causes noticeable modulation at normal intensity.	Positive-going signal decreases intensity.
Usable Frequency Range	Dc to 50 MHz. ^a	
Input Resistance		25 k Ω , within 10%. Decreases to approximately 200 Ω at 2 MHz and above.
Maximum Input Voltage		25 V (dc — peak ac).

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
SIGNAL OUTPUTS		
CH 1 VERT SIGNAL OUT		
Output Voltage	At least 50 mV per division of displayed signal into 1 M Ω . At least 25 mV per division of displayed signal into 50 Ω .	
Output Resistance		Approximately 50 Ω .
Bandwidth	Dc to at least 50 MHz into 50 Ω .	
DC Level	Approximately 0 V.	Within 100 mV.
A and B — GATES		
Output Voltage	Approximately 5.5 V, positive-going rectangular pulse.	Starts at 0 V, within 500 mV.
Output Resistance		Approximately 500 Ω .

Table 8 (cont)

Characteristics	Performance Requirements	Supplemental Information
POWER SOURCE		
AC-Source Voltage Ranges		
115 V		
(High)	108 V to 132 V. ^a	
(Low)	90 V to 110 V. ^a	
230 V		
(High)	216 V to 250 V. ^a	
(Low)	198 V to 242 V. ^a	
AC-Source Frequency	48 Hz to 440 Hz. ^a	
Power Consumption		
Typical	115 watts (140 VA). ^a	
Maximum	150 watts (190 VA). ^a	48 Hz, 110 Vac, low regulating range.

Table 8 (cont)

INTERNAL POWER SUPPLIES

Characteristics	Supplemental Information			
	Initial Setting	Any 500-Hour Period After First 200 Hours	Maximum p-p Ripple	Accuracy From -15°C to 55°C
Main Supply Accuracy ($+20^{\circ}\text{C}$ to $+30^{\circ}\text{C}$)	8 V	$\pm 0.9\%$	$\pm 1.7\%$	2 mV
	+5 V	$\pm 0.9\%$	$\pm 1.7\%$	2 mV
	-15 V	$\pm 0.9\%$	$\pm 1.7\%$	2 mV
	-55 V	$\pm 0.3\%$	$\pm 0.7\%$	4 mV
	-2450 V	$\pm 1.2\%$	$\pm 2.2\%$	100 mV
Digital Storage Power Supplies (Not Adjustable)	+110 V	$\pm 3\%$		
	Voltage	-6 V	-12 V	+5 V
	Tolerance	+4%	$\pm 5\%$	$\pm 4\%$
Maximum p-p Ripple				150 mV

* Performance requirement not checked in manual.