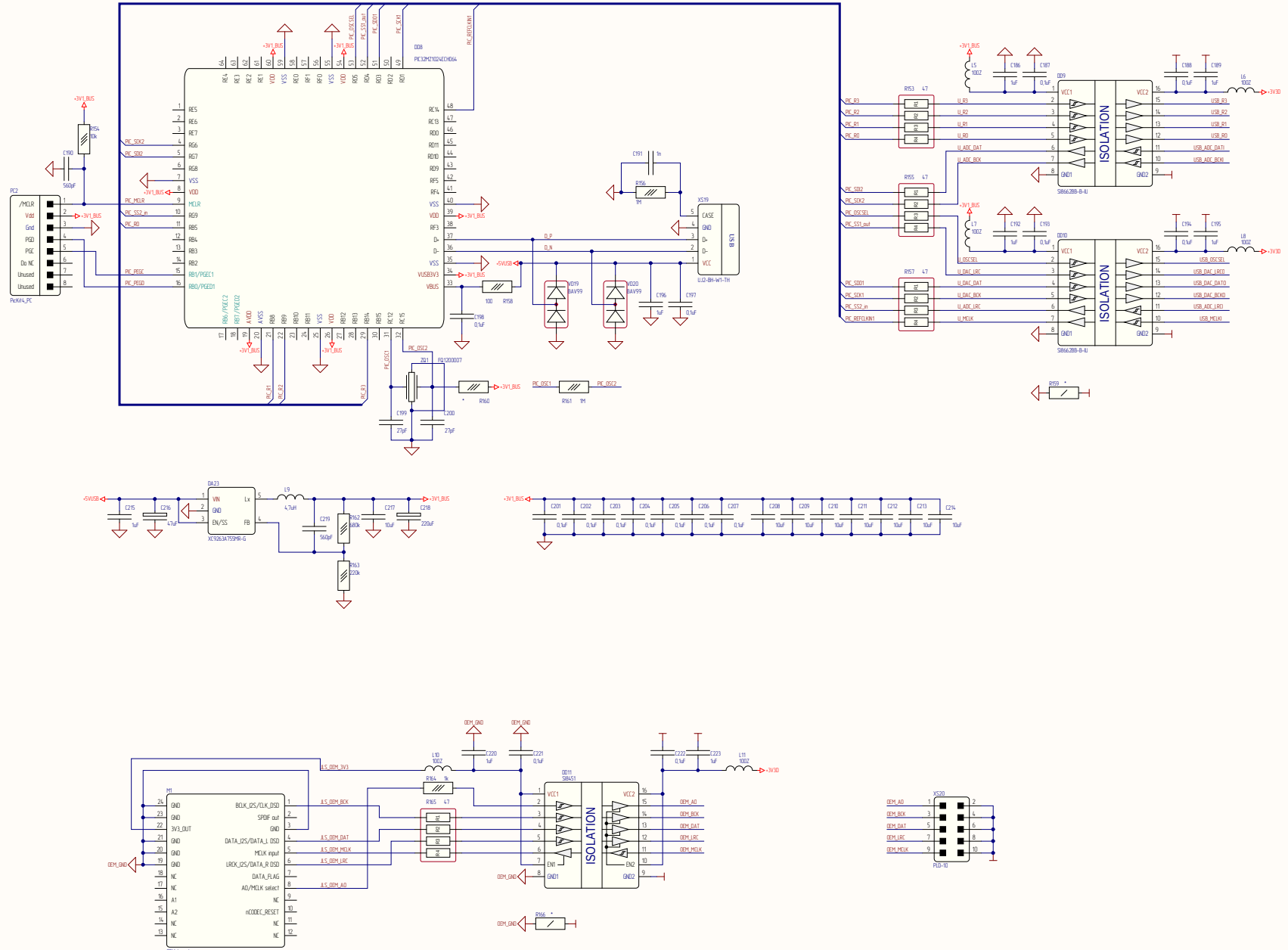
DD8
PIC32MZ1024ECH064
PIC_SCK1
PIC_SDO1
PIC_SS1_out
PIC_SS1_in
PIC_RESTLKIN1
PIC_SDI2
PIC_SCK2
PIC_SS2_in
PIC_RD
PIC_MCLR
PIC_PEGC
PIC_PEGD
PIC_OSC1
PIC_OSC2
PIC_OSCSEL
PIC_CR1
PIC_CR2
PIC_CR3
+3V1_BUS
+5VUSB
D_P
D_N
VUSB3V3
VBUS
PC2
PicKit4_PC
XS9
USB
U.232-BH-W1-TH
DD9
DD10
ISOLATION
SI8662BB-B-IU
R953
R955
R957
R959
U_R3
U_R2
U_R1
U_R0
U_ADC_DAT
U_ADC_BCK
U_OSCSEL
U_DAC_LRCK
U_DAC_DAT
U_DAC_BCK
U_ADC_LRC
U_MCLK
USB_R3
USB_R2
USB_R1
USB_R0
USB_ADC_DAT1
USB_ADC_BCK0
USB_OSCSEL
USB_DAC_LRCK
USB_DAC_DATA
USB_DAC_BCK0
USB_ADC_LRC
USB_MCLK0
+3V3D
DA23
XC9263A75SMR-G
L9
4,7uH
R962
680k
R963
220k
C219
560pF
C217
10uF
C218
220uF
C215
1uF
C216
4,7uF
C201
C202
C203
C204
C205
C206
C207
C208
C209
C210
C211
C212
C213
C214
0,1uF
10uF
M1
OEM_board
OEM_GND
I2S_OEM_3V3
I2S_OEM_BCK
I2S_OEM_DAT
I2S_OEM_MCLK
I2S_OEM_LRC
I2S_OEM_A0
BCLK_I2S/CLK_DSD
SPDIF out
DATA_I2S/DATA_L DSD
MCLK input
LRCK_I2S/DATA_R DSD
DATA_FLAG
A0/MCLK select
nCODEC_RESET
3V3_OUT
DD11
SI8451
R964
R965
R966
OEM_A0
OEM_BCK
OEM_DAT
OEM_LRC
OEM_MCLK
XS20
PLD-10
Title
Size
A2
Number
Revision
Date:
20.12.2022
Sheet of
File:
D:\Projects\...\MOTHER_BOARD_USB_V2.SchDoc
Drawn By: