

5	4	3	2	1															
PA0 BCLK_MEAS Timer Inp, High Speed, Pull-DN		PB0 RELAY Output, LowSpeed, 0 at start		PC13 SW1 Input, Pull-Up															
PA1 INP_SW Output, LowSpeed, 0 at start		PB1 ANP_EN Output, LowSpeed, 0 at start		PC14 SW2 Input, Pull-Up															
PA2 I2S_Z Output, LowSpeed, 0 at start		PB2 LED1 Output, LowSpeed, 0 at start		PC15 SW3 Input, Pull-Up															
PA3 VOL2 Analog Input (ADC)		PB3 Not used now, Z-state. Reserved for SPI																	
PA4 Not used		PB4 Not used																	
PA5 SCALE Input, Pull-DN		PB5 Not used now, Z-state. Reserved for SPI																	
PA6 nMUTE Input, Pull-DN		PB7 P_EN Output, LowSpeed, 0 at start																	
PA7 nDSD_ON Input, Pull-Up		PB8 SCL DAC I2C																	
PA8 Lock_DAC Input, Pull-DN		PB9 SDA DAC I2C																	
PA9 SW4 Input, Pull-Up		PB10 LED2 Output, LowSpeed, 0 at start																	
PA10 DFU_EN Input, Pull-DN		PB11 LED3 Output, LowSpeed, 0 at start																	
PA11 USB_DM USB in Bootloader		PB12 IRC Timer Inp, High Speed, Pull-UP																	
PA12 USB_DP USB in Bootloader		PB13 ICLK SPI Clock, HighSpeed																	
PA13 SWDIO SWD Debugger		PB14 ILATCH Output, HighSpeed, 1 at start																	
PA14 SWCLK SWD Debugger		PB15 IMOSI SPI DataOut, HighSpeed																	
PA15 nDACRST Output, LowSpeed, 0 at start																			
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