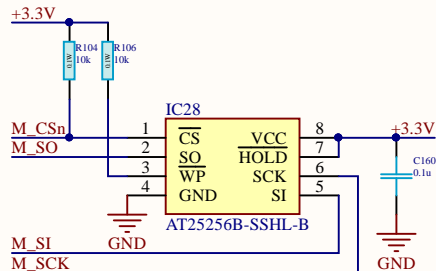
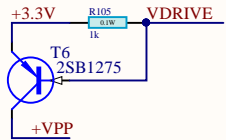


+3.3V

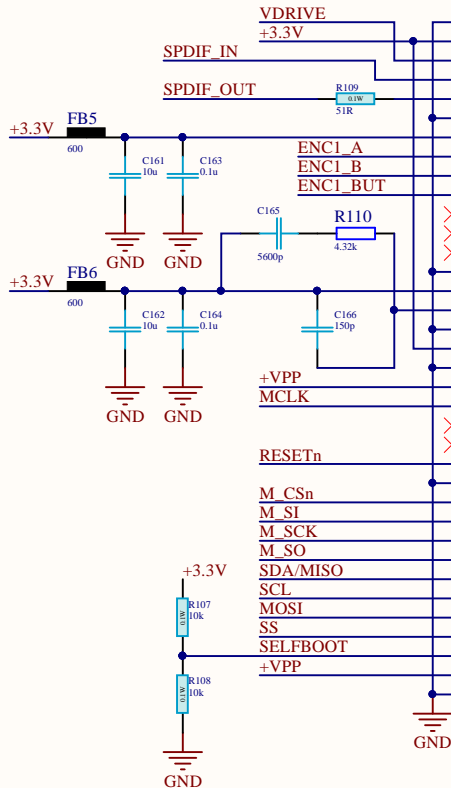
+3.3V

RST RESETn
MCLK MCLK
SPDIF_IN SPDIF_IN
SPDIF_OUT SPDIF_OUT



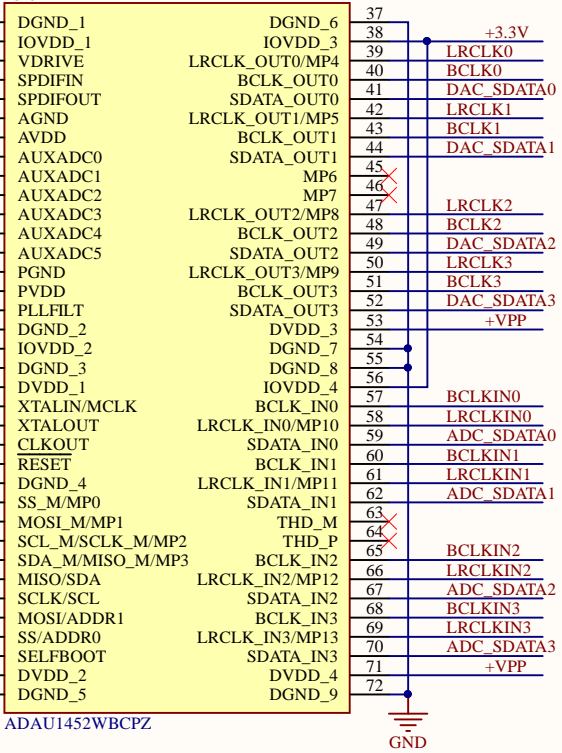
SDA/MISO SDA/MISO
SCL SCL

ENC1_A ENC1_A
ENC1_B ENC1_B
ENC1_BUT ENC1_BUT



Устанавливать только R107

IC29



ADAU1452WBCPZ

DAC_SDAT3 DS4
DAC_SDAT2 DS3
DAC_SDAT1 DS2
DAC_SDAT0 DS1
BCLK BCLK
LRCLK LRCLK

BCLK0 R111 OR BCLK
BCLK1 R112 OR BCLK
BCLK2 R113 OR BCLK
BCLK3 R114 OR BCLK

LRCLK0 R115 OR LRCLK
LRCLK1 R116 OR LRCLK
LRCLK2 R117 OR LRCLK
LRCLK3 R118 OR LRCLK

ADC_SDAT1 ADC_SDAT1
ADC_SDAT0 ADC_SDAT0
BCLKIN BCLKIN
LRCLKIN LRCLKIN

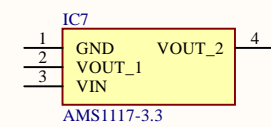
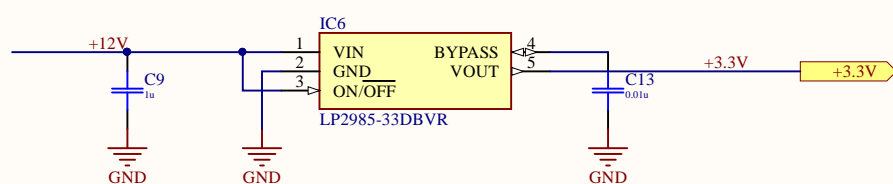
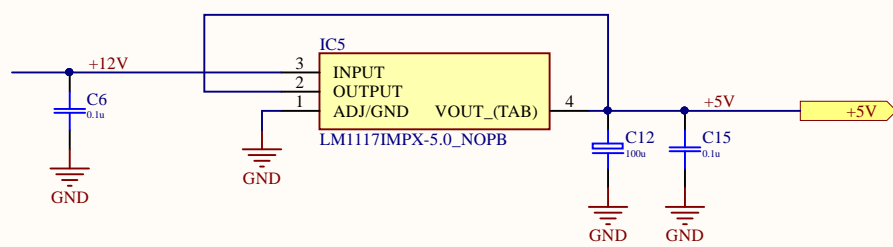
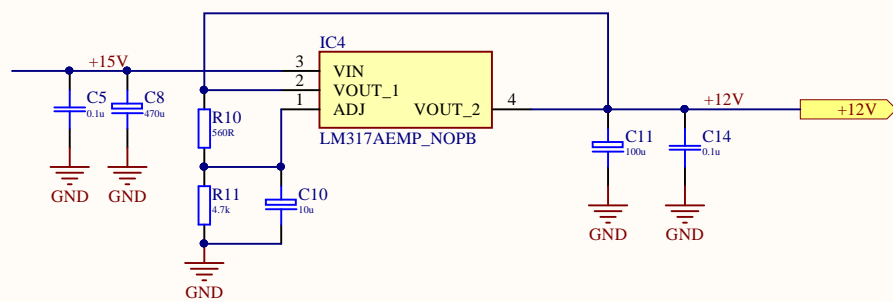
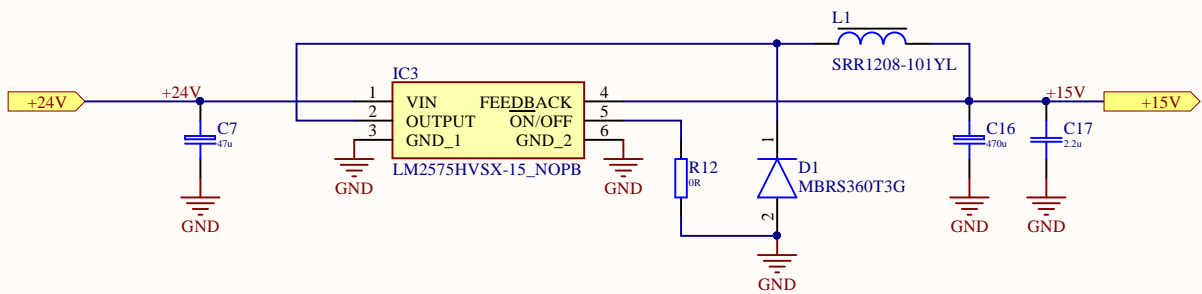
BCLKIN0 R119 OR BCLKIN
BCLKIN1 R120 OR BCLKIN

LRCLKIN0 R121 OR LRCLKIN
LRCLKIN1 R122 OR LRCLKIN

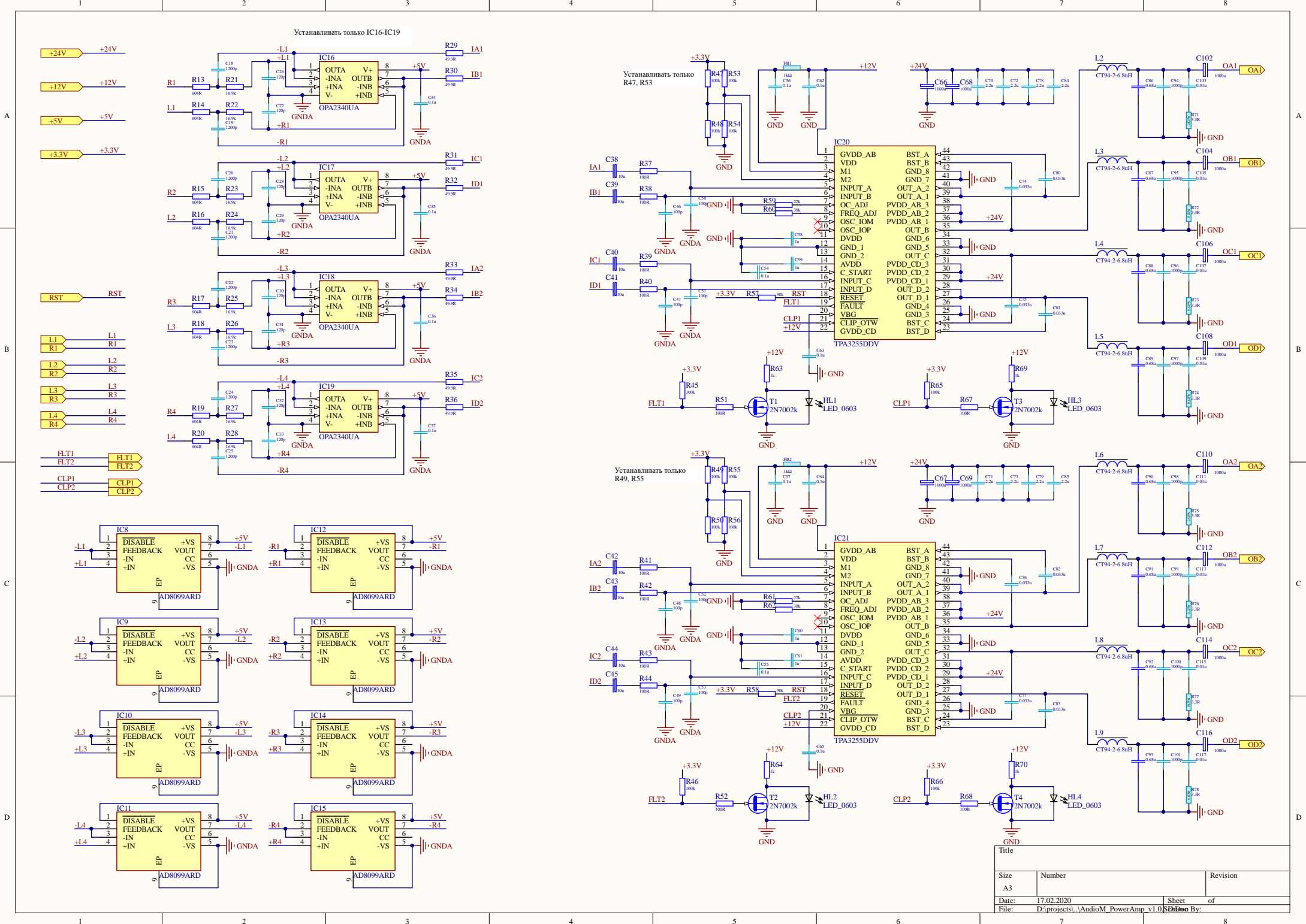
Устанавливать только R111, R115, R119, R121

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File:	D:\projects\AudioM_ADAU_v1.0.Sch	Drawn By:

Title		
Size A3	Number	Revision
Date: 17.02.2020	Sheet	of
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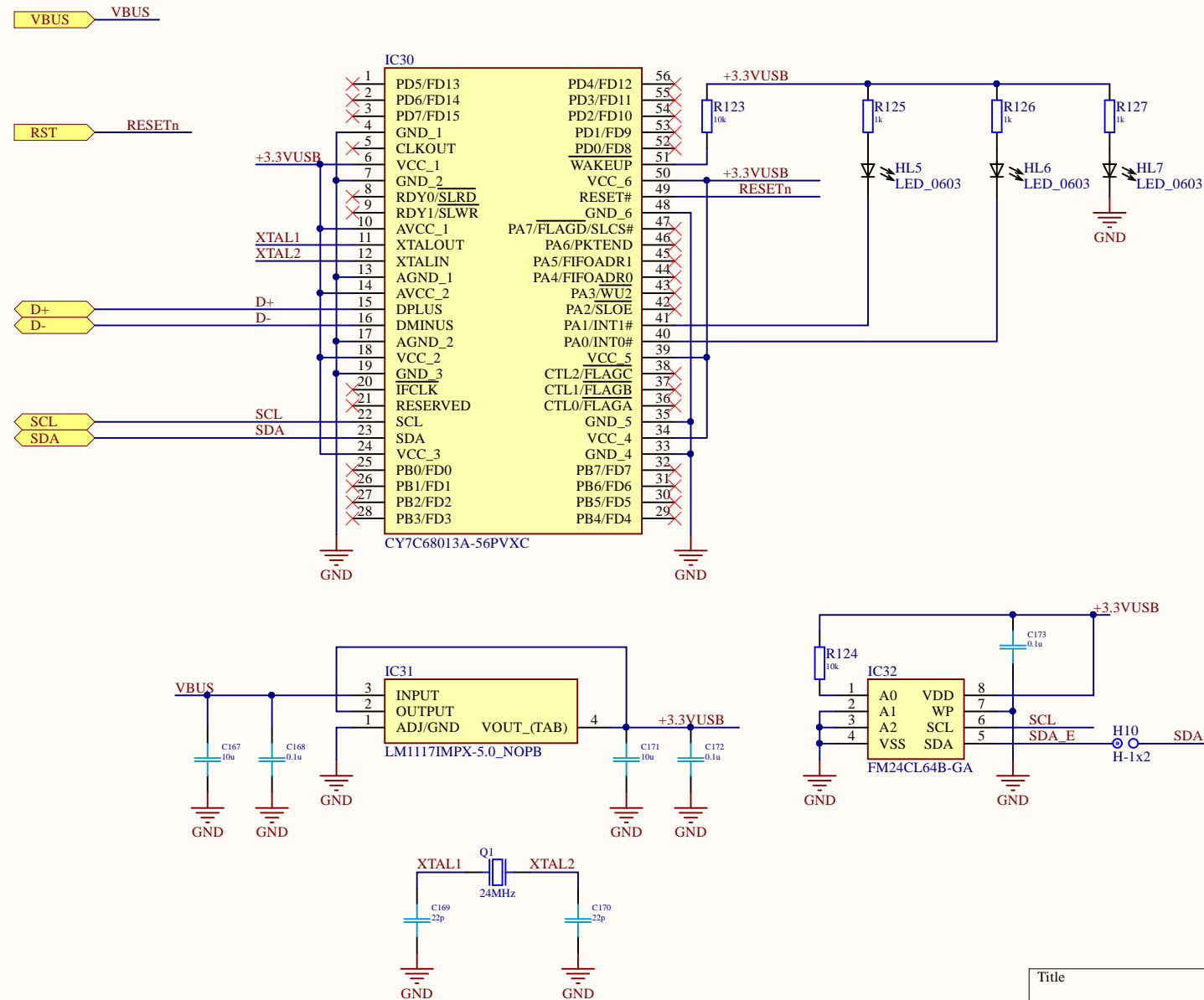


1

2

3

4



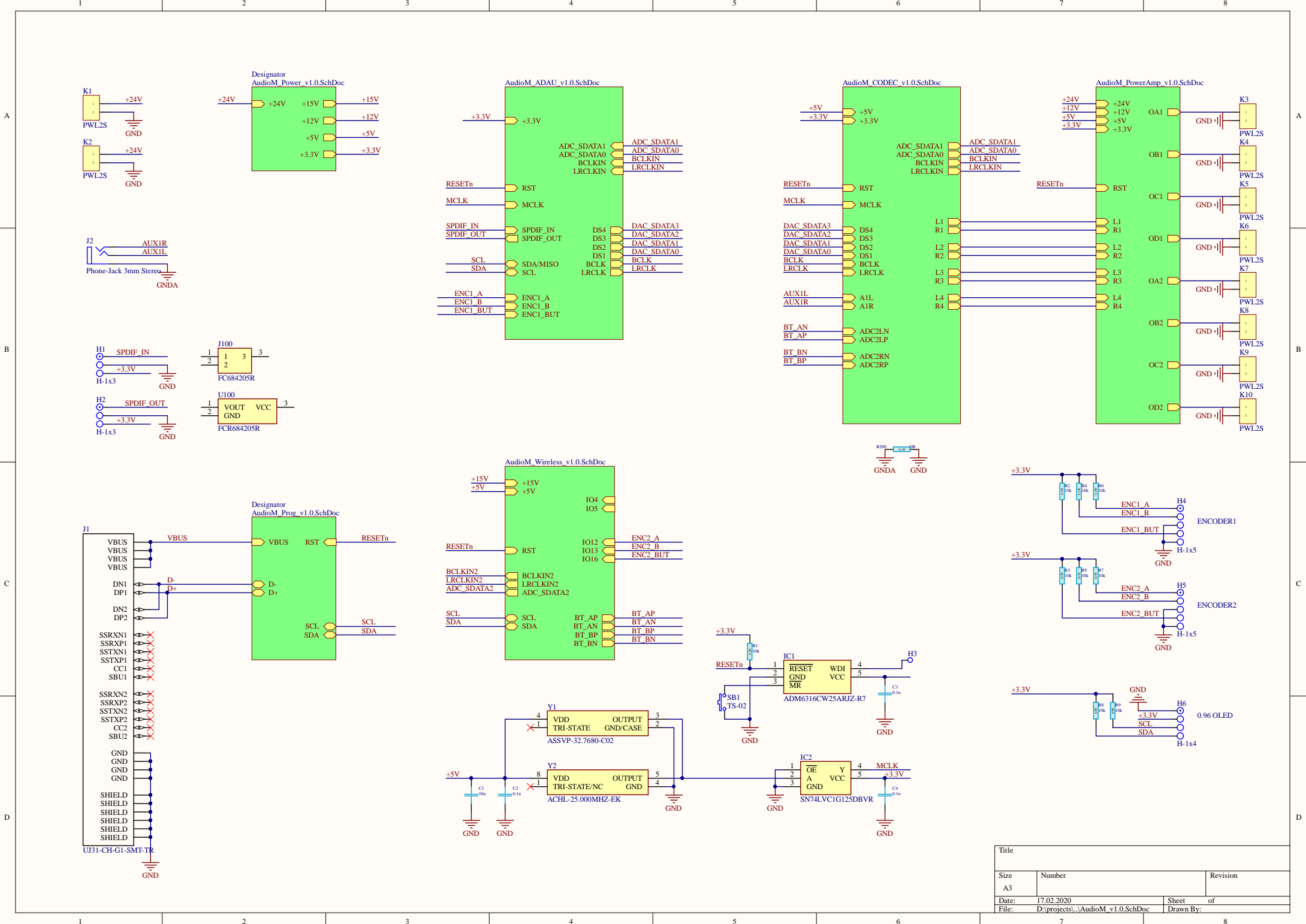
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2

3

4



Title		
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