

Pin#	Pin Name	I/O	PU/PD	Description	Connect to
38	AVDD	I		Analog power supply (3.3 V)	A3.3V
1	AVSS			Analog ground	AGND
7	CS0	I	Pulldown	Chip select	NC
9	DVDD	I		Digital power supply input (3.3 V)	D3.3V
30	DVDD	I		Digital power supply input (3.3 V)	D3.3V
29	DVSS			Digital ground	DGND
10	DVSS			Digital ground	DGND
8	GPIO	I/O	Pullup	GPIO control (user programmable)	?
19	LRCLK	I/O	Pulldown	Sample rate clock (fS)	L/R CLK Out (DAC/I2S) + 47ohm to L/R WM8804
5	MCLKIN	I		Master clock input (connect to ground when not in use)	PD Config.
21	MCLKO	O		Master clock output	MCLK Out (DAC/I2S)
6	MICROCLK_DIV	I	Pulldown	Internal microprocessor clock divide control	NC?
31	PDN	I	Pullup	Power down.	Shutdown? To MCU?
34	PLL0	I	Pullup	PLL control 0	PD Config.
35	PLL1	I	Pulldown	PLL control 1	PU Config.
36	PLL2	I	Pullup	PLL control 2	PD Config.
33	RESERVED			Reserved. Connect to ground	DGND
37	RESERVED			Reserved. Connect to ground	DGND
32	RESET	I	Pullup	Reset, active low	Reset. To watchdog? MCU?
16	SCL1	I/O		I2C port 1 clock (always a slave)	MCU I2C Clock IN
18	SCL2	I/O		I2C port 2 clock (always a master)	EEPROM I2C Clock OUT
20	SCLKIN	I	Pulldown	Bit clock input	WM8804 Bit Clock OUT
22	SCLKOUT1	O		Bit clock 1 out. Used to receive input serial data.	WM8804 Bit Clock IN (as slave)
23	SCLKOUT2	O		Bit clock 2 out. Used to clock output serial data.	DAC/I2S Bit Clock IN (as slave)
15	SDA1	I/O		I2C port 1 data (always a slave)	MCU I2C Data OUT
17	SDA2	I/O		I2C port 2 data (always a master)	EEPROM I2C Data IN
11	SDIN1	I	Pulldown	Serial data input 1	WM8804
12	SDIN2	I	Pulldown	Serial data input 2	NC?
13	SDIN3	I	Pulldown	Serial data input 3	NC?
14	SDIN4	I	Pulldown	Serial data input 4	NC?
27	SDOUT1	O		Serial data output 1	DAC/I2S
26	SDOUT2	O		Serial data output 2	DAC/I2S
25	SDOUT3	O		Serial data output 3	DAC/I2S
24	SDOUT4	O		Serial data output 4	DAC/I2S
2	VR_PLL			Internal regulator. This pin must not be used to power external devices.	0.01u to AGND
3	XTALI	O		Oscillator input (connect to ground when not in use)	
4	XTALO	O		Oscillator output	
28	VR_DIG			Internal regulator. This pin must not be used to power external devices.	4.7u to DGND