

## Dr. JKILL

### Настройка параметров.

Параметры JKILL настраиваются двумя способами:

- 1) через меню, доступное при управлении энкодером - по длительному нажатию кнопки, при управлении кнопками - одновременным длительным нажатием верхней и нижней.
- 2) Программой-конфигуратором на PC. Полученный файл записывается на USB-флеш, после чего записывается в JKILL в режиме апгрейда.

Список параметров и их значение:

| Group                   | Parameter       | Values                                            | Description                                                                                                                 |
|-------------------------|-----------------|---------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------|
| BUS Format              | DAC OUT 44/48   | I2S, LJ, RJ24, RJ16                               | I2S OUT Format for each FS                                                                                                  |
|                         | DAC OUT 88/96   | I2S, LJ, RJ24, RJ16                               |                                                                                                                             |
|                         | DAC OUT 176/192 | I2S, LJ, RJ24, RJ16                               |                                                                                                                             |
|                         | DAC OUT DSD     | Enable/Disable                                    | DSD Output Enable/Disable (NI)                                                                                              |
| BUS Control             | DAC x2x3 I2S    | 00,01,10,11                                       | X2, X3 signals status for each bus format                                                                                   |
|                         | DAC x2x3 LJ     | 00,01,10,11                                       |                                                                                                                             |
|                         | DAC x2x3 RJ24   | 00,01,10,11                                       |                                                                                                                             |
|                         | DAC x2x3 RJ16   | 00,01,10,11                                       |                                                                                                                             |
| Digital Filter Control  | DAC DF 44/48    | 00,01,10,11                                       | X0, X1 signals statusfor each Fs                                                                                            |
|                         | DAC DF 88/96    | 00,01,10,11                                       |                                                                                                                             |
|                         | DAC DF 176/192  | 00,01,10,11                                       |                                                                                                                             |
|                         | Input Format    | I2S, LJ, RJ24                                     | Input I2S Format                                                                                                            |
|                         | Remote Control  | None, IR, BT, IR+BT                               | Use and type of Remote Control (NAI)                                                                                        |
| Signal Processing       | Oversampling    | None, OV96, OV192, X2                             | NI                                                                                                                          |
|                         | Dithering       | Enable/Disable                                    | Use dithering                                                                                                               |
|                         | 16 bit Detect   | Enable/Disable                                    | Signal Width Detector                                                                                                       |
|                         | VU Enable       | Enable/Disable                                    | VU (Signal Level) Display View                                                                                              |
|                         | Silent Detect   | Enable/Disable                                    | Use Silence Detector                                                                                                        |
|                         | Silent Time,ms  | time in ms                                        | Silence Detector Timeout                                                                                                    |
|                         | Volume Control  | None, P16, P32, P64, S16, S32, S64, A16, A32, A64 | Use of the Volume control Steps number Control Type (Parallel, Serial SPI, Serial RC-compatible (Antecom Volume PCB), (NAI) |
| Inputs Setup            | SPDIF Inputs    | None, 1, 1+2                                      | Using of the Coaxial and Optical Inputs                                                                                     |
|                         | TOSLINK Inputs  | None, 1, 1+2                                      |                                                                                                                             |
|                         | Ext.I2S Input   | Enable/Disable                                    | Using of the External I2S Input                                                                                             |
|                         | Analog Input    | Enable/Disable                                    | Use of the Analog Input (with ADC)                                                                                          |
| I2S Input Control Lines | Inp Scale @44   | +/-                                               | Input FS Scale polarity for 44kHz line                                                                                      |
|                         | Inp.Mute pol.   | +/-                                               | Input Mute signal polarity (==mute)                                                                                         |
|                         | Use Mute        | +/-                                               | Using of the external Mute Signal                                                                                           |
|                         | Demph. pol.     | +/-                                               | Input Demph signal polarity (==Demph)                                                                                       |
|                         |                 |                                                   |                                                                                                                             |

|                        |                 |                     |                                        |
|------------------------|-----------------|---------------------|----------------------------------------|
| Output Control signals | Out Mute pol.   | +/-                 | Output Mute Signal polarity (==mute)   |
|                        | Out Demph. Pol. | +/-                 | Output Demph signal polarity (==Demph) |
|                        | Reset@Mute      | +/-                 | Make DAC Reset when mute ?             |
|                        | OFF Control     | +/-                 | Use                                    |
| ADC                    | ADC Master/SL   | M/S                 | External ADC Mode (TBI)                |
|                        | ADC Slave FS    | 44.1-192            | Ext. ADC FS for Slave Mode (NI)        |
|                        | ADC Bus Format  | I2S, LJ, RJ24, RJ16 | Ext. ADC Bus Format                    |
|                        | Use Ext DSD     | En/Dis              | Using External DSD Source (NI)         |
| Hardware               | Display Type    | Normal/Inverted     | 2 types TFT Settings                   |
|                        | Encoder Type    | Normal/Inverted     | Encoder direction settings             |

Preliminary