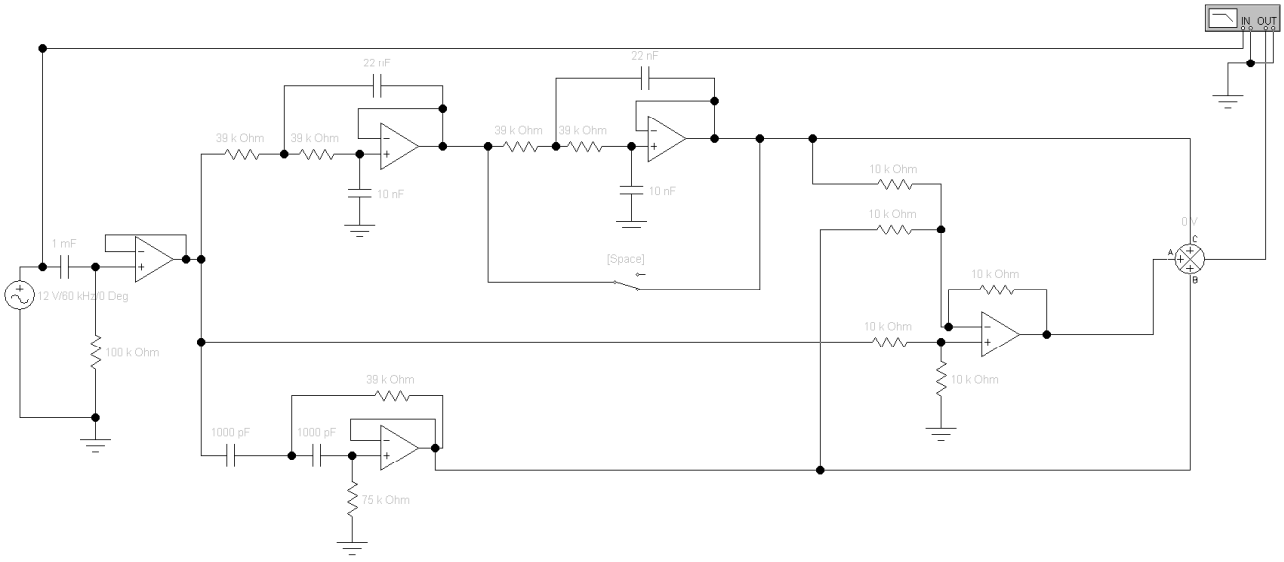

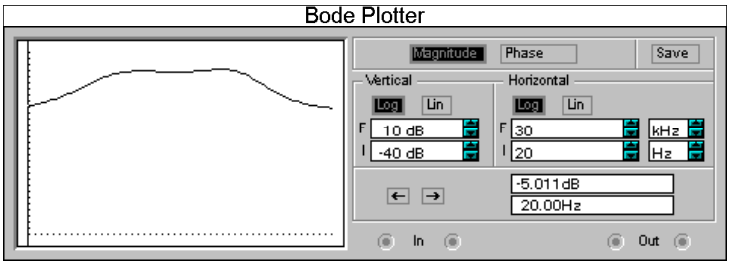

3-Terminal Opamp Model "ideal" Library "default"

A	Open-loop gain	1e+06	
RI	Input resistance	1e+10	Ω
RO	Output resistance	1	Ω
VSW+	Positive voltage swing	20	V
VSW-	Negative voltage swing	-20	V
VOS	Input offset voltage	0	V
IBS	Input bias current	0	A
IOS	Input offset current	0	A
SR	Slew rate	1e+10	V/s
FU	Unity-gain bandwidth	1e+12	Hz
FP2	Location of second pole	0	Hz
CC	Compensation capacitance	0	F

Parts list

Description	Quantity	Reference ID
3-Terminal Opamp, ideal	5	AR5, AR1, AR3, AR4, AR2
AC Voltage Source, 12V, 60kHz, 0Deg	1	V1
Capacitor, 22nF	2	C13, C3
Capacitor, 1000pF	2	C5, C4
Capacitor, 10nF	2	C14, C2
Capacitor, 1mF	1	C1
Connector	27	
Ground	6	0
Node	16	
Resistor, 100kΩ, 0Ω/°C, 0Ω/°C²	1	R1
Resistor, 39kΩ, 0Ω/°C, 0Ω/°C²	5	R13, R2, R12, R5, R3

Resistor, 75kΩ, 0Ω/°C, 0Ω/°C²	1	R6
Resistor, 10kΩ, 0Ω/°C, 0Ω/°C²	5	R9, R11, R10, R7, R8
Switch, [Space]	1	S1
Three-Way Voltage Summer, 0V, 0V, 0V, 1V/V, 0V/V, 0V/V, 1V/V, 0V	1	A1